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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yang, et al. Docket No.: TSM03-0929
Serial No.: 10/823,158 Art Unit: 2814
Filed: April 13, 2004 Examiner: Theresa T. Doan
For: Methods and Structures for Planar and Multiple-Gate Transistors Formed on SOI

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Amendment

TD

Dear Sir:

8/21/06.

Applicants respectfully submit the following amendments and remarks in response to Examiner's Office Action dated June 16, 2006, which Action was made final. In order to expedite allowance of the present application, Applicants have herein cancelled all rejected claims. The only remaining claims are claim 39, which was previously allowed, and newly added claims 40 - 48, which depend from allowed claim 39. Hence, it is believed that the application has been placed in condition for issuance. Applicants respectfully request that this amendment be entered pursuant to the provisions of 37 C.F.R. § 1.116.

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